

PL500/PLE500

The new PLC generation

Pixsys PLC series PL500/PLE500 is relying on a modular and flexible concept. CPU **PL500** is the control unit and connectivity node, basing on ARM CORTEX A8 -1 GHz microprocessor and featuring extensive range of serial ports and protocols: RS485, RS232 (Modbus RTU), Ethernet (Modbus TCP/IP), CanOpen.

Multiple combinations of analogue-digital I/O are available on **PLE500 modules**. Communication among CPU and I/O modules is basing on a real-time internal Bus on DIN-rail.

Flagship of Pixsys PLC range are the PID modules with programmable analogue input, currently available as single loop but already planned in additional versions with multiple loops.

Development environment **LogicLab**, compliant with IEC 61131 standards, is now the same for this PLC series as well as for Pixsys HMI and Panel PC, thus ensuring scalability of applications, optimal synergy among different series and connectivity with third-party hardware thanks to wide list of drivers already included.

The extensive library of Pixsys devices allows their straightforward and easy integration into applications.



User friendly, multimedia support and tr

For the complete range of Pixsys products we provide [technical support](#) for installation, programming via Skype.

Programming tutorial videos are available on our [youtube channel](#).

As for the entire range of PIXSYS products, thanks to the QR code the traceability of the product is guaranteed. Information and documentation online can also be accessed by reading the QR CODE that directs you to technical specifications and verifies its warranty conditions.



Ordering codes

PL500-335-1AD	PLC DIN Rail CPU 1 Ethernet, 1 RS485, 1 RS232, 1 CANopen
PLE500-6AD	PL500 Expansion 16 Digital I/O, 2 AI, 2 AO
PLE500-7AD	PL500 Expansion 1 AI, 2 DO
PLE500-8AD	PL500 Expansion 1 AI, 2 DO, 1 AO

Main features

Box	DIN43880 - 3 module 54x90x64 (PL500, PLE500-6AD), 1 module 18x90x64 (PLE500-7AD, PLE500-8AD)
Power supply	12..24 V DC +/- 15%
Power consumption	3 W
Operating conditions	Temperature 0-45 °C, humidity 35..95 RH%

Material	Noryl V0
Weight	Approx. 250 g
Sealing	IP20 box
Terminal blocks	Extractable

PL500 technical data

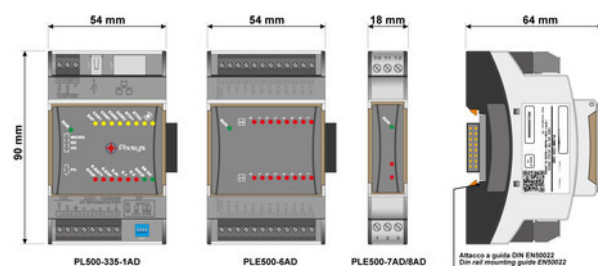
Processor	ARM Cortex A8-1 GHz
Memory	Flash 4 GB/ RAM 512 MB DDR3
Memory Card	Micro SD
Expansion Bus	PLE-DIN-BUS (real time) on DIN RAIL
Field bus	RS485 (Modbus RTU Master/Slave), RS232 (Modbus RTU Master/Slave), CANopen (Master), Modbus TCP/IP (Master/Slave)
USB	1 port for PLC programming/update
Ethernet	1 port 10/100 Base-T on RJ45 for PLC programming and for Modbus TCP/IP communication

PLE500-6AD technical data

Digital input	16 PNP (selectable)
Digital output	16 PNP (selectable)
Encoder input	4 (Res. 32 bit, 50 KHz) Push Pull

Analogue input	2 (Res. 16 bit) (40000 points) 4..20 mA / 0..10V
Bus Protocol	PLE-DIN-BUS (real time) on DIN RAIL
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<h2>PLE500-7AD technical data</h2>	
Digital output	2 PNP
Analogue input	1 (Res. 16 bit) selectable: TC type K,S,R,J, PT100, PT500, PT1000, Ni100, PTC1K, NTC10K, (β 3435K), 0..10 V (54000 points), 0/4..20mA (40000 points), 0..60 mV (16000 points, potentiometer 6 K Ω , 150 K Ω (50000 points)
Bus Protocol	PLE-DIN-BUS (real time) on DIN RAIL

Installation



Size and installation